

Surface Mount Schottky Barrier Rectifier Reverse Voltage - 20 to 200 V Forward Current - 2.0A

#### **Features**

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- · Low power loss, high efficiency
- · High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications
- · Hireliability application and automotive grade AEC-Q101 qualified

#### **MECHANICAL DATA**

· Case: SOD-123FL

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight:15mg 0.00048oz

### Absolute Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified Single phase, half wave, 60Hz resistive or inductive load

Maximum DC Blocking Voltage         V <sub>DC</sub> 20         40         60         80         100         120         150         200           Maximum Average Forward Rectified Current @ Fig.1         I <sub>F(AV)</sub> 2.0         2.0           Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)         I <sub>FSM</sub> 50           Peak Forward Surge Current,1.0ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)         I <sub>FSM</sub> 100           I²t Rating for fusing (3ms≤t≤8.3ms)         I²t         10.3	Parameter	Symbols	AT-DS22 WM	AT-DS24 WM	AT-DS26 WM	AT-DS28 WM	AT-DS210 WM	AT-DS212 WM	AT-DS215 WM	AT-DS220 WM	Units
Maximum DC Blocking Voltage         V <sub>DC</sub> 20         40         60         80         100         120         150         200           Maximum Average Forward Rectified Current @ Fig.1         I <sub>F(AIV)</sub> 2.0         2.0           Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)         I <sub>FSM</sub> 50           Peak Forward Surge Current,1.0ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)         I <sub>FSM</sub> 100           I²t Rating for fusing (3ms≤t≤8.3ms)         I²t         10.3	aximum Repetitive Peak Reverse Voltage	$V_{RRM}$	20	40	60	80	100	120	150	200	V
Maximum Average Forward Rectified Current @ Fig.1 $I_{F(AV)}$ 2.0       Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method) $I_{FSM}$ 50       Peak Forward Surge Current,1.0ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method) $I_{FSM}$ 100 $I^2$ t Rating for fusing (3ms≤t≤8.3ms) $I^2$ t     10.3	aximum RMS voltage	V <sub>RMS</sub>	14	28	42	56	70	84	105	140	V
@ Fig.1       IF(AV)       2.0         Peak Forward Surge Current,8.3ms       Single Half Sine-wave Superimposed on Rated Load (JEDEC method)       IFSM       50         Peak Forward Surge Current,1.0ms       Single Half Sine-wave Superimposed on Rated Load (JEDEC method)       IFSM       100         I²t Rating for fusing (3ms≤t≤8.3ms)       I²t       10.3	aximum DC Blocking Voltage	V <sub>DC</sub>	20	40	60	80	100	120	150	200	V
Single Half Sine-wave Superimposed on Rated Load (JEDEC method)     I <sub>FSM</sub> 50       Peak Forward Surge Current,1.0ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)     I <sub>FSM</sub> 100       I²t Rating for fusing (3ms≤t≤8.3ms)     I²t     10.3	•	I <sub>F(AV)</sub>		2.0				Α			
Single Half Sine-wave Superimposed on Rated Load (JEDEC method)  I²t Rating for fusing (3ms≤t≤8.3ms)  I³t  100	ngle Half Sine-wave Superimposed	I <sub>FSM</sub>		50				А			
	ngle Half Sine-wave Superimposed	I <sub>FSM</sub>	100					А			
Max Instantaneous Forward Voltage at 2 A $V_F$ 0.55 0.65 0.85 0.95	Rating for fusing (3ms≤t≤8.3ms)	l <sup>2</sup> t	10.3				A <sup>2</sup> S				
	ax Instantaneous Forward Voltage at 2 A	V <sub>F</sub>	0.9	55	0.65	0.8	85		0.95		V

0.1

5

86

73

105

25 32

-55 ~ +150

-55 ~ +150

130

-	′1۱	Measured at 1	MHz and	annliad	roverce	voltage of A	VDC
- 1		measured at 1	IVII IZ allu	applied	1646136	VUILAGE OF T	v D.C

<sup>(2)</sup> P.C.B. mounted with 0.2" X 0.2" (5 X 5 mm) copper pad areas.

T<sub>a</sub> = 25°C

Ta =100°C

 $I_R$ 

 $C_{j}$ 

 $\overline{R_{\theta JA}}$ 

Rejc

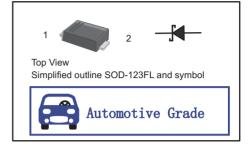
 $R_{\theta JL}$ 

 $T_{i}$ 

 $T_{\text{stg}}$ 

#### **PINNING**

PIN	DESCRIPTION		
1	Cathode		
2	Anode		



0.1

3

40

Maximum DC Reverse Current

at Rated DC Reverse Voltage

Typical Junction Capacitance

Typical Thermal Resistance

Storage Temperature Range

Operating Junction Temperature Range

mA

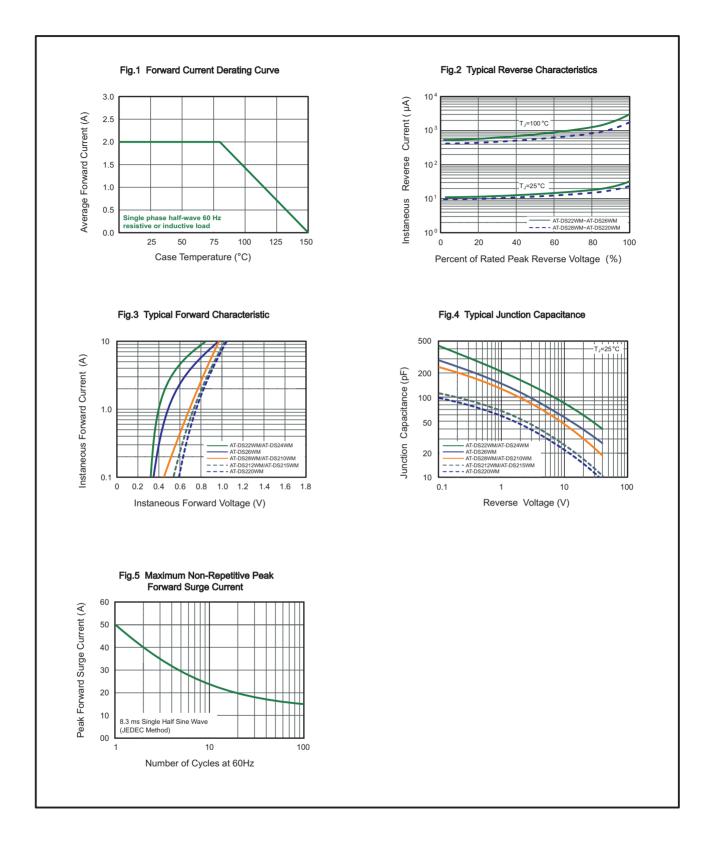
рF

°C/W

°C

°C

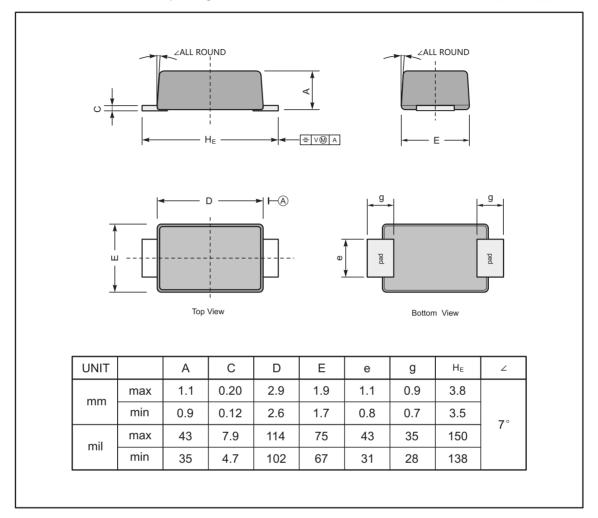
35



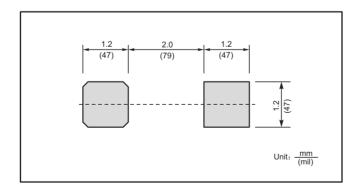
# PACKAGE OUTLINE

## Plastic surface mounted package; 2 leads

SOD-123FL



## The recommended mounting pad size



## Marking

Type number	Marking code
AT-DS22WM	S22
AT-DS24WM	S24
AT-DS26WM	S26
AT-DS28WM	S28
AT-DS210WM	S210
AT-DS212WM	S212
AT-DS215WM	S215
AT-DS220WM	S220

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